

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

Fee et al.

Serial No.: 10/728,413

Filed: December 5, 2003

For: QUAD FLAT NO LEAD (QFN) GRID
ARRAY PACKAGE, METHOD OF
MAKING AND MEMORY MODULE AND
COMPUTER SYSTEM INCLUDING SAME

Confirmation No.: 9766

Examiner: A. Chambliss

Group Art Unit: 2814

Attorney Docket No.: 2269-4738.1US
(00-1113.01/US)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

May 9, 2005
Date


Signature

Leta M. Howard
Name (Type/Print)

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following amendments and remarks are filed in response to the Examiner's remarks in the Office Action mailed February 7, 2005, the three-month shortened statutory period for response to which expires on May 9, 2005, May 7th being a Saturday.

Amendments to the Title appear on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 6 of this paper.

IN THE TITLE:

The title has been amended herein. Pursuant to 37 C.F.R. §§ 1.121 and 1.125 (as amended to date), please enter the title as amended.

QUAD FLAT NO LEAD (QFN) GRID ARRAY PACKAGE, ~~METHOD OF MAKING AND~~
~~MEMORY MODULE AND COMPUTER SYSTEM INCLUDING SAME~~